



## PATENT Docket No. INTEL/17479

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	) I hereby certify that this paper and the documents referred to as enclosed
Kiran R. DESAI	) therewith are being deposited with the ) United States Postal Service as first
U.S. Serial No.: 10/630,164	) class mail, postage prepaid, in an ) envelope addressed to Commissioner
For: "Methods and Apparatus for	) for Patents, P.O. Box 1450,
Maintaining Cache	) Alexandria, Virginia 22313-1450 on
Coherency"	) this date:
Filed: July 30, 2003	) October 21, 2004
Assignee: Intel Corporation	} from G. Flott
Group Art Unit: 2186	) James A. Flight
Examiner: Not Yet Assigned	Attorney for Applicant(s) Registration No. 37,622

## STATUS LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter.

Respectfully submitted,

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October 21, 2004

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